Detailed Description of Preferred Embodiment

To first refer to Figures 1 to 4, there is shown a portable digital device 10 such as that disclosed in our earlier US patent application serial number 10/100,351 filed 18 March 2002 for "Memory Module With Audio Playback Mode", the contents of which are hereby incorporated by reference.

The device 10 includes a play/pause button 12 for initiating playback, and pausing playback. It may also be used to stop playback, and to be an on/off switch. A digital display 14 is also provided. It may be, for example, a liquid crystal display ("LCD"). Device 10 also has a volume up button 16, volume down button 18, and a menu control knob 20. Knob 20 may be spring loaded to a central position and is able to be rotated anticlockwise and clockwise to move a cursor to the right and to the left across a menu page displayed across display 14; and may be depressed for selection of menu pages, and icons representing various functions within each menu page. It may also be used for track skipping both forwards and reverse.

Device 10 also has an earphone/headphone socket 22, and an in-built microphone 24.

As is shown in Figure 3, the display 14 has data displayed 15 so oriented so that the display 15 is to be read by a person using control aspects of buttons 16, 18, and 20 using their right hand. This would be the normal situation for a right-handed user.

Figure 4 shows the device 10 with the display 14 having the data displayed 15 reversed as if the device 10 is being used by the left hand of a person, the normal situation where the user is left-handed. The data displayed 15 is displayed as a bit map. By rotation of the bit map 180°, the display is "flipped" for left-hand use. This reverse process is used to rotate displayed data 15 from left hand to right hand use.

In Figure 5, the display 14 is controlled by a processor 26 having an operating system. Memory 28 is preferably a non-volatile memory. The mappable control functions of controls 12, 16, 18 and 20 are also controlled by processor 26. A digital amplifier and audio circuit 25 may also be provided controlled by processor 26.

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